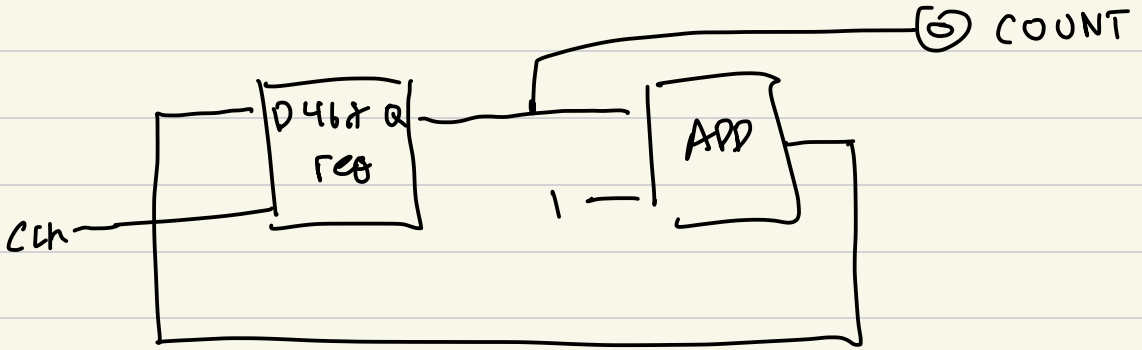


# CS315-02 Lab Latches FlipFlops Registers



SR Latch

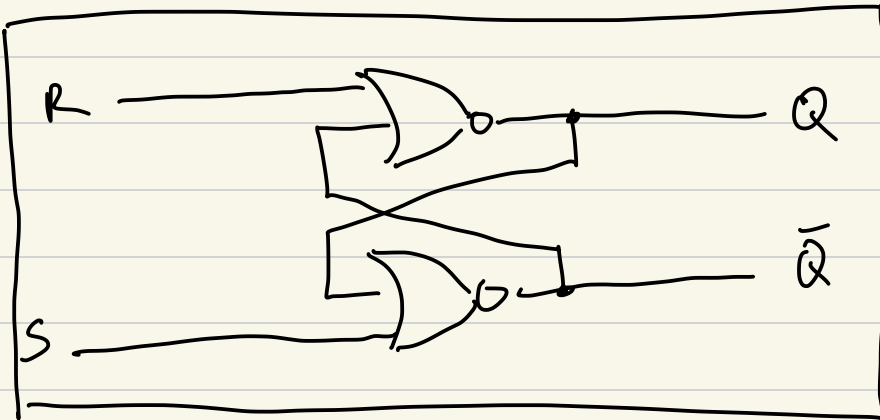
D Latch (clock)

D FlipFlop

1 bit register

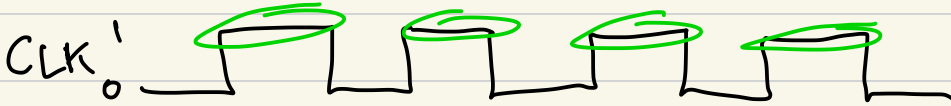
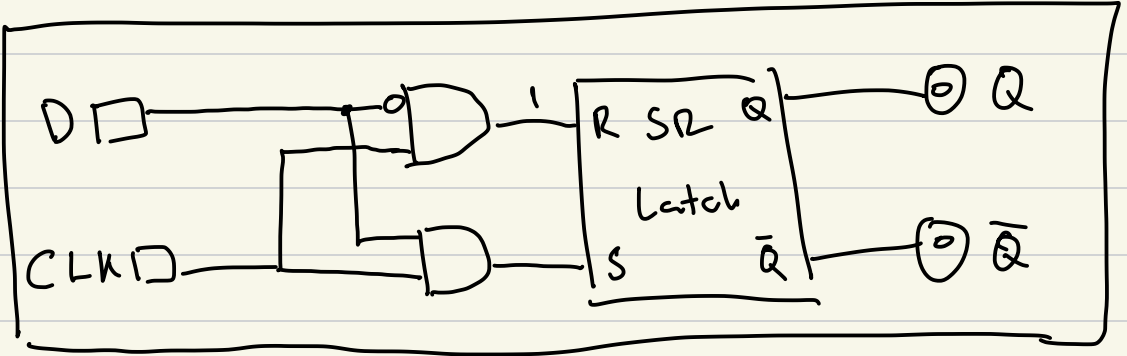
4 bit register

SR Latch

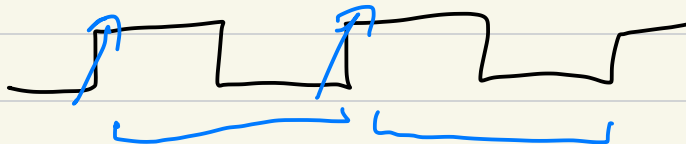
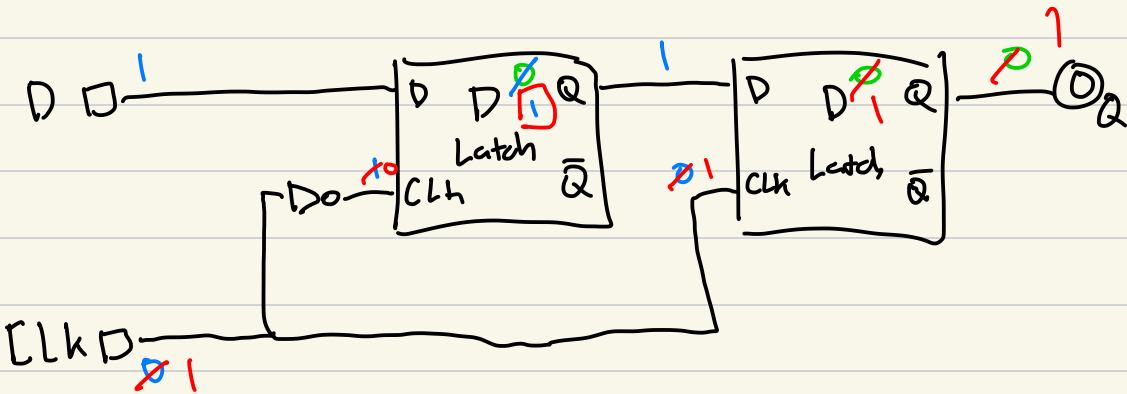


RS	Q Q̄
10	01
00	01
01	10
00	10
11	XX

# D Latch



# D Flip Flop

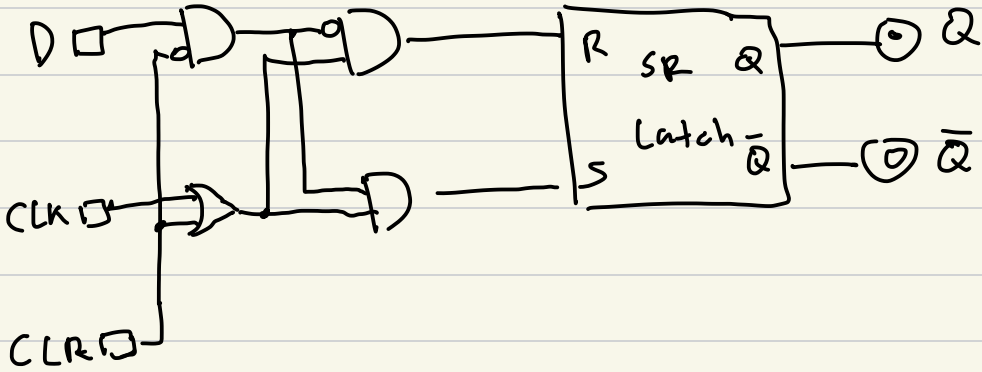


# 1 bit Register

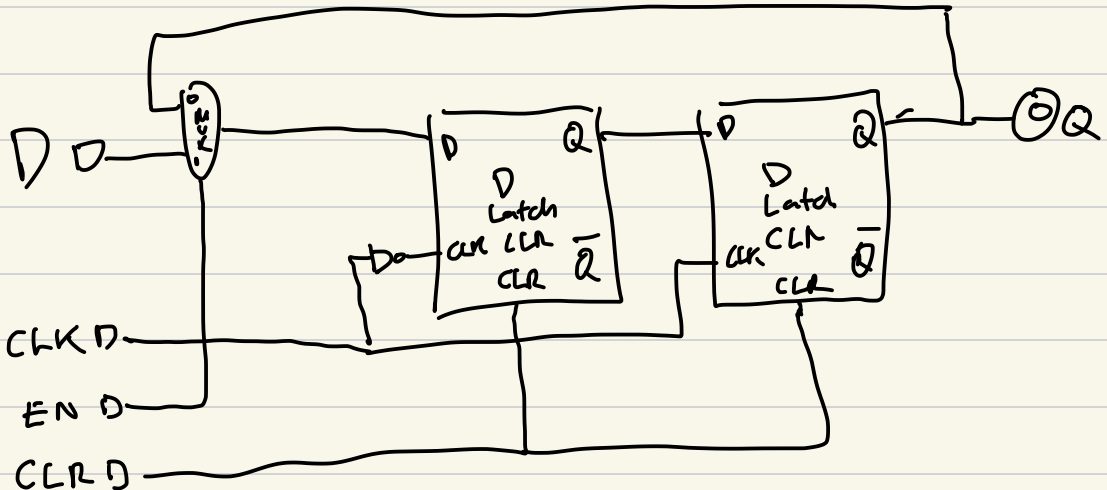
CLR  
EN

Clear / reset  
Enable

## D Latch with CLR



## 1 bit register



# N bit Register

